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(71) Applicant (for all designated States except US): **THOMSON LICENSING S.A.** [FR/FR]; 46, Quai A. Le Gallo, Boulogne 92648 (FR).

(72) Inventor; and

(75) Inventor/Applicant (for US only): **BOUILLET, Aaron** [US/US]; 1520 Persimmon Place, Noblesville, Indiana 46060 (US).

(74) Agents: **TRIPOLI, Joseph et al.**; c/o Thomson Licensing, Inc., Two Independence Way, Suite 200, Princeton, New Jersey 08540 (US).

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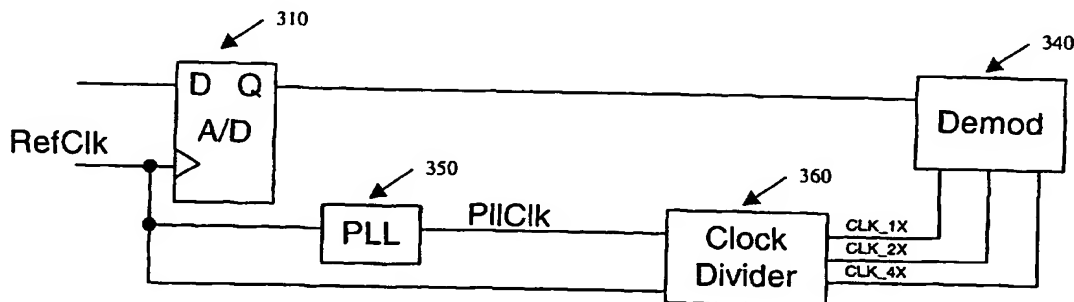
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(54) Title: A METHOD FOR USING A SYNCHRONOUS SAMPLING DESIGN IN A FIXED-RATE SAMPLING MODE



(57) Abstract: The present application generally relates to apparatuses such as television signal processing apparatus that process radio frequency signals. More specifically, the present application is particularly useful in integrated circuits that must combine circuitry operating in a synchronous-sampling mode that must be adapted for use with a fixed rate sampling mode application. According to an exemplary embodiment, the television signal processing apparatus 100 comprises a source of a fixed rate digital signal 220, signal processing circuitry 240 operating in a synchronous-sampling mode wherein the signal processing circuitry 240 comprises a signal representing a symbol rate, and an interpolator 230 for processing the fixed rate digital signal to yield samples at the symbol rate.

# INTERNATIONAL SEARCH REPORT

International application No.

PCT/US04/01581

## A. CLASSIFICATION OF SUBJECT MATTER

IPC(7) : G06F 11/30; H04L 9/00, 9/32

US CL : 713/200

According to International Patent Classification (IPC) or to both national classification and IPC

## B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)

U.S. : 713/200

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practicable, search terms used)

## C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category *	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US 5,943,369 (Knutson et al) 24 August 1999 (24.08.1999), column 1 lines 23-25; column 2 lines 11-14; column 10 line 58-62; column 4 line 63-66; column 10 line 1-3	1-3, 6-11, 14-15 and 18-20
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Y		
Y	US 3,943,346 (Urkowitz et al) 9 March 1976 (09.03.1976), column 8 line 53-67; column 2 line 26-28	4-5, 12-13, 16-17 4, 12 and 16
Y	US 2002/0154620 (Azenkot et.al) 24 October 2002 (24.10.2002), paragraph [0375]	5, 13 and 17

☐ Further documents are listed in the continuation of Box C.

☐ See patent family annex.

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"O" document referring to an oral disclosure, use, exhibition or other means	"&" document member of the same patent family
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Mail Stop PCT, Attn: ISA/US  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, Virginia 22313-1450

Facsimile No. (703) 305-3230

Authorized officer

Ayaz R Sheikh

Telephone No. 703-305-0900